

UNITED STATES PATENT AND TRADEMARK OFFICE



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/600,779	06/20/2003	John M. Fike	QN1008.US	5982
7590 09/28/2004		EXAMINER		
Tejinder Singh			WAMSLEY, PATRICK G	
Klein, O'Neill & Singh, LLP Suite 510		ART UNIT	PAPER NUMBER	
2 Park Plaza Irvine, CA 92614			2819	
			DATE MAILED: 09/28/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Action Commons	10/600,779	FIKE, JOHN M.				
Office Action Summary	Examiner	Art Unit				
	Patrick G. Wamsley	2819				
The MAILING DATE of this communication appeariod for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	6(a). In no event, however, may a reply be tim within the statutory minimum of thirty (30) days ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	ely filed will be considered timely. the mailing date of this communication. (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on	1) Responsive to communication(s) filed on					
2a) This action is FINAL . 2b) ⊠ This	a) This action is FINAL . 2b) This action is non-final.					
3) Since this application is in condition for allowan) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4)⊠ Claim(s) <u>1-12</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-12</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or	election requirement.					
Application Papers						
9) The specification is objected to by the Examiner.						
10)⊠ The drawing(s) filed on <u>20 June 2003</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Ex	• • • • • • • • • • • • • • • • • • • •	, ,				
Priority under 35 U.S.C. § 119						
12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority documents have been received.						
 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage 						
<u> </u>	•	ed in this National Stage				
application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
See the attached detailed Office action for a list of	of the certified copies not receive	u.				
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview Summary					
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) 	Paper No(s)/Mail Da 5) Notice of Informal P	ite atent Application (PTO-152)				
Paper No(s)/Mail Date	6) Other:					

Application/Control Number: 10/600,779

Art Unit: 2819

g - 100 8

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-12 are rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent 5,736,949 to Ong et al, hereafter Ong.

For claim 1, as depicted in Figure 2a, Ong internally couples a bandgap reference circuit to the input node of an analog to digital converter, hereafter ADC.

For claim 5, CPU systems inherently use ADCs.

For claim 9, ADCs are conventionally provided upon application specific integration circuits, hereafter ASICs.

For claims 2, 6, and 10, Ong provides power supply and reference voltage sources. Both voltage values are inherently positive.

For claims 3, 7, and 11, while Ong's related art uses a divider [column 2], Ong's invention is not required to use one [column 3, lines 57-62].

For claims 4, 8, and 12, Ong's bandgap reference voltage is inversely proportional to the power supply voltage. Specifically, reference voltages will vary relative to an absolute value, such as the bandgap of silicon [column 4, lines 33-37]. If the power supply decreases, the band-gap reference will be proportionately increased.

Application/Control Number: 10/600,779 Page 3

Art Unit: 2819

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. U.S. Patents 6,535,054 to Ceekala and 6,462,612 to Roh et al disclose bandgap reference circuits. U.S. Patent 6,445,330 to Thomsen et al calibrates ADCs with ASICs having capacitive dividers. U.S. Patent 6,445,315 to den Breejen calibrates ADCs with measurement ASICs having CMOS bandgap references. U.S. Patent 5,281,866 to Rundel shows a band gap circuit for an ADC.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Patrick G. Wamsley whose telephone number is (571) 272-1814. The official facsimile number is (703) 872-9306.

Patrick G. Warnsley

September 23, 2004